



UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office  
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SERIAL NUMBER	FILING DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NO.
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08/999,498 12/29/97 HARARI

E

EXAMINER

020227 MM11/0917  
MAJESTIC PARSONS SIEBERT & HSUE  
FOUR EMBARCADERO CENTER SUITE 1100  
SAN FRANCISCO CA 94111-4106

TRAN. A

ART UNIT

PAPER NUMBER

2818

7

DATE MAILED: 09/17/98

### NOTICE OF ALLOWABILITY

#### PART I

1. ☒ This communication is responsive to PRE-AMENDS OF 12/29/97, 04/07/98 & 06/08/98
2. ☒ All the claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice Of Allowance And Issue Fee Due or other appropriate communication will be sent in due course.
3. ☒ The allowed claims are 63-89 (CLAIMS 1-62 and 90-99 CANCELED)
4. ☐ The drawings filed on \_\_\_\_\_ are acceptable.
5. ☐ Acknowledgment is made of the claim for priority under 35 U.S.C. 119. The certified copy has [ ] been received. [ ] not been received. [ ] been filed in parent application Serial No. \_\_\_\_\_ filed on \_\_\_\_\_
6. ☒ Note the attached Examiner's Amendment.
7. ☐ Note the attached Examiner Interview Summary Record, PTOL-413.
8. ☒ Note the attached Examiner's Statement of Reasons for Allowance.
9. ☒ Note the attached NOTICE OF REFERENCES CITED, PTO-892.
10. ☒ Note the attached INFORMATION DISCLOSURE CITATION, PTO-1449.

#### PART II

A SHORTENED STATUTORY PERIOD FOR RESPONSE to comply with the requirements noted below is set to EXPIRE THREE MONTHS FROM THE "DATE MAILED" indicated on this form. Failure to timely comply will result in the ABANDONMENT of this application. Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

1. ☐ Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL APPLICATION, PTO-152, which discloses that the oath or declaration is deficient. A SUBSTITUTE OATH OR DECLARATION IS REQUIRED.
2. ☒ APPLICANT MUST MAKE THE DRAWING CHANGES INDICATED BELOW IN THE MANNER SET FORTH ON THE REVERSE SIDE OF THIS PAPER.
  - a. ☒ Drawing informalities are indicated on the NOTICE RE PATENT DRAWINGS, PTO-948, attached hereto or to Paper No. \_\_\_\_\_. CORRECTION IS REQUIRED.
  - b. ☐ The proposed drawing correction filed on \_\_\_\_\_ has been approved by the examiner. CORRECTION IS REQUIRED.
  - c. ☐ Approved drawing corrections are described by the examiner in the attached EXAMINER'S AMENDMENT. CORRECTION IS REQUIRED.
  - d. ☒ Formal drawings are now REQUIRED.

Any response to this letter should include in the upper right hand corner, the following information from the NOTICE OF ALLOWANCE AND ISSUE FEE DUE: ISSUE BATCH NUMBER, DATE OF THE NOTICE OF ALLOWANCE, AND SERIAL NUMBER.

#### Attachments:

- ☒ Examiner's Amendment
- ☒ Examiner Interview Summary Record, PTOL-413
- ☒ Reasons for Allowance
- ☒ Notice of References Cited, PTO-892
- ☒ Information Disclosure Citation, PTO-1449

- ☒ Notice of Informal Application, PTO-152
- ☒ Notice re Patent Drawings, PTO-948
- ☐ Listing of Bonded Draftsmen
- ☐ Other

*[Handwritten signature]*

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#7/D  
at  
09/16/98

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The Preliminary Amendments filed respectively on December 29, 1997, April 07, 1998 and June 08, 1998 have been received and placed of record.

Claims 1-62 and 90-99 been canceled.

Claims 63-89 pending.

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

In the claims :

In claim 63, line 12, change "associated" to --coupled--;

In claim 71, line 2, after "and", add --a--;

line 3, change "that are" to --coupled to said

memory controller and said system address bus, said multiple sectors being--;

line 10, after "circuit", add --, coupled to said multiple sectors and--;

line 14, after "coupled to", add --said multiple sectors to--;

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In claim 77, line 8, after "circuit", add --, coupled to said multiple sectors and--;

line 11, after "coupled to", add --said multiple sectors to--;

In claim 82, line 6, change "combinations" to --combination--;

line 15, change "@" to --(c)--;

line 16, change "combinations" to -- combination--; and

In claim 85, line 4, change "includes" to --is terminated by--.

The above changes are to render the claims clearer and complete, without reference to prior art.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Gerald P. Parsons on September 16, 1998.

The following is an examiner's statement of reasons for allowance:

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The prior art, alone or in combination, fails to teach or fairly suggest a Flash EEPROM system comprising one or more integrated circuit chips each having an array of Flash EEPROM cells partitioned into a plurality of sectors, each sector addressable for erase such that all cells therein are erasable simultaneously, the cells of the array being individually programmable into more than two states in order to store more than one bit of data per cell; means for selecting a plurality of sectors among the one or more chips for erase operation; means for simultaneously performing the erase operation on only the plurality of selected sectors; and individual register coupled to each sector for holding a status to indicate whether the sector is selected or not.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."


at

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September 16, 1998

  
David Nelms  
Supervisory Patent Examiner  
Technology Center 2800



UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office

**NOTICE OF ALLOWANCE AND ISSUE FEE DUE**

020227 MM11/0917  
MAJESTIC PARSONS SIEBERT & HSUE  
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SAN FRANCISCO CA 94111-4106

APPLICATION NO.	FILING DATE	TOTAL CLAIMS	EXAMINER AND GROUP ART UNIT	DATE MAILED
08/999,498	12/29/97	027	TRAN, A	2818 09/17/98
First Named Applicant HARARI, ELIYAHOU				

TITLE OF INVENTION  
MULTI-STATE FLASH EEPROM SYSTEM WITH SELECTIVE MULTI-SECTOR ERASE  
(AS AMENDED)

ATTY'S DOCKET NO.	CLASS-SUBCLASS	BATCH NO.	APPLN. TYPE	SMALL ENTITY	FEE DUE	DATE DUE
2	365-185.030	J23	UTILITY	NO	\$1320.00	12/17/98

**THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED.**

**THE ISSUE FEE MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED.**

**HOW TO RESPOND TO THIS NOTICE:**

**I. Review the SMALL ENTITY status shown above.**

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

- A. If the status is changed, pay twice the amount of the FEE DUE shown above and notify the Patent and Trademark Office of the change in status, or
- B. If the status is the same, pay the FEE DUE shown above.

If the SMALL ENTITY is shown as NO:

- A. Pay FEE DUE shown above, or
- B. File verified statement of Small Entity Status before, or with, payment of 1/2 the FEE DUE shown above.

**II. Part B-Issue Fee Transmittal should be completed and returned to the Patent and Trademark Office (PTO) with your ISSUE FEE. Even if the ISSUE FEE has already been paid by charge to deposit account, Part B Issue Fee Transmittal should be completed and returned. If you are charging the ISSUE FEE to your deposit account, section "4b" of Part B-Issue Fee Transmittal should be completed and an extra copy of the form should be submitted.**

**III. All communications regarding this application must give application number and batch number. Please direct all communications prior to issuance to Box ISSUE FEE unless advised to the contrary.**

***IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.***